



SREE VAHINI INSTITUTE OF SCIENCE AND TECHNOLOGY
(AN AUTONOMOUS INSTITUTION)

Department of Electrical & Electronics Engineering

Minutes of the 7th Board of Studies (BOS) Online Meeting held on
1st October 2026 at 3:00 P.M

Agenda of the Board of Studies Meeting

1. Approval of B.Tech Regulation V26 for 2026 Batch onwards.
2. Approval of B.Tech EEE- Course Structure & Syllabus of I B. Tech I & II Sem (V26)
3. Approval of B.Tech EEE-Course outcomes of I B. Tech I & II Sem.
4. Approval of BEEE & BEEE Lab Course Structure and Syllabus for CSE-Allied Branches (V26)
5. Approval of PEE & PEE Lab Course Structure and Syllabus for ECE Branch (V26)
6. Any other matter with permission of the Chair

Note: Detailed resolutions are enclosed.

S. No	Name of the Member	Designation	Signature
1	Dr. K. Kiran Kumar HOD of EEE	BOS-Chairman	
2	Dr. K. Sri Kumar Professor, EEE, UCEV, JNTU-GV, Vizianagaram.	Member (University Nominee)	Present-Online
3	Dr. D V S S Siva Sarma Professor, Dept. of EEE, NITW, Warangal.	Member (Subject Expert)	Present-Online
4	Dr. CH. Ravi Kumar, HOD, Dept of EEE, ANU, Guntur Andhra Pradesh	Member (Subject Expert)	Present-Online
5	Mr. S. Prathap Assistant Executive Engineer APGENCO, Vidyut soudha, Vijayawada	Subject Expert (Industry)	Present-Online
6	Mr. A. Hema Sundar Kumar AEE, APCPDCL, Tiruvuru Rural	Expert (Outside)	Present-Online
7	Mr. B. Kutumba Rao	Alumni	-Absent-

8	Mrs. K. Ramya	Internal Member	K. Ramya
9	Mr. P. Uday Kumar	Internal Member	Uday
10	Mr. V Murali Krishna	Internal Member	Murali Krishna
11	Mr. SK. Baji	Internal Member	SK Baji
12	Mrs. M. Hari Priya	Internal Member	M. Hari Priya
13	Mr. M. Naga Raju	Internal Member	M. Naga Raju
14	Mrs. Mamatha	Internal Member	Mamatha

Dr. K. Kiran Kumar, Head of the Department, Electrical and Electronics Engineering, Sree Vahini Institute of Science and Technology, Tiruvuru, chaired the meeting. He expressed his sincere thanks to the entire BOS Members for their consent to be on the Board of Studies of the department and their valuable suggestions for improvement. He extended a warm and hearty welcome to all the honorable members of the Board of Studies.

He acknowledged all the members for their valuable suggestions, advice, guidelines, and concern for the overall development of the department. Later, he presented the credentials of the college and the agenda of the meeting.

The Board of Studies (BOS) reviewed and approved the Course Structure and Syllabus for I B.Tech I & II Semesters (V26) Regulations. The suggestions and recommendations made by the BOS members were discussed in detail and duly incorporated. Accordingly, the syllabus was revised and approved by the Board for implementation.

I B.Tech I Sem Course Structure (V26)- EEE Branch

Course Structure

B.Tech. – I Year I Semester (I semester)

S.No.	Course Category	Course Code	Title	L	T	P	Credits
1	BSC		Linear Algebra and Calculus	3	0	0	3
2	BSC		Physics for Electrical Systems	3	0	0	3
3	ESC		Computational Thinking and Problem Solving with C	3	0	0	3
4	ESC		Basic Electrical Circuits	3	0	0	3
5	HSM		English for Technical Communication	3	0	0	3
6	BSC		Physics for Electrical Systems Lab	0	0	3	1.5
7	ESC		Computational Thinking and Problem Solving with C Lab	0	0	3	1.5
8	HSM		English for Technical Communications and Moodles Lab	0	0	3	1.5
9	ESC		Basic Electrical Circuits Lab	0	0	3	1.5
10	MNC		NSS / NCC / Community Service	0	0	2	0
Total				15	0	14	21

I B.Tech II Sem Course Structure (V26)-EEE Branch

B.Tech (I year - II Semester) – II semester

S.No	Course Category	Course Code	Title	L	T	P	Credits
1	BSC		Differential Equations and Vector calculus	3	0	0	3
2	BSC		Chemistry for Electrical Systems	3	0	0	3
3	PCC		Electrical Circuit Analysis	3	0	0	3
4	PCC		Semiconductor Devices	3	0	0	3
5	ESC		Engineering Graphics	0	1	3	2.5
6	BSC		Chemistry for Electrical Systems Lab	0	0	3	1.5
7	PCC		Electrical Circuit Analysis Lab	0	0	3	1.5
8	ESC		Design Thinking and Innovation	0	1	3	2.5
9	ESC		IT & Electronic Workshop	0	0	2	1
10	MNC		Sports and Yoga	0	0	2	0
Total				12	2	16	21

Suggestions Given by the Board of Studies (BOS) Members

Suggestions and Recommendations Given by the Board of Studies (BOS) Members

The following suggestions and recommendations were made by the BOS members regarding the revision and improvement of the course syllabi and laboratory experiments.

❖ Dr. CH. Ravi Kumar suggested the Following:

1. Basic Electrical Circuits (I B.Tech – I Semester, EEE Branch)

- **Unit II: Introduction to Electrical Circuits** – Remove the duplicate content related to Node Analysis and Mesh Analysis.

❖ Dr. K. Sri Kumar suggested the Following:

2. Electrical Circuit Analysis Lab (I B.Tech – II Semester, EEE Branch)

- Replace the **Two-Wattmeter Method** with the **Three-Wattmeter Method** for measuring power in unbalanced loads.

3. Basic Electrical and Electronics Engineering (I B.Tech – I Semester, CSE Allied Branch)

- **Unit II: AC Circuits** – Revise the topic from “Voltage and current relationship with phasor diagrams in R, L, and C series circuits only” to “Voltage and current relationships with phasor diagrams in R, L, and C individual circuits and RLC series circuits.”
- **Unit IV: Semiconductor Devices and Electronic Circuits** – Include BJT NPN and PNP configurations in the Common Emitter (CE) configuration.

4. Basic Electrical and Electronics Engineering Lab (I B.Tech – I Semester, CSE Allied Branch)

- **Interchange Experiments 10 and 11** as follows:
 - ✓ **Experiment 10:** Plot the V-I characteristics of a PN junction diode.
 - ✓ **Experiment 11:** Investigate the characteristics of a Zener diode as a voltage regulator.
- **Revise Experiment 13** as follows:
 - ✓ **Experiment 13:** Analyze the input and output waveforms of a full-wave bridge rectifier.

❖ Dr. D. V. S. S. Siva Sarma suggested the Following:

5. Updating Reference and Textbooks

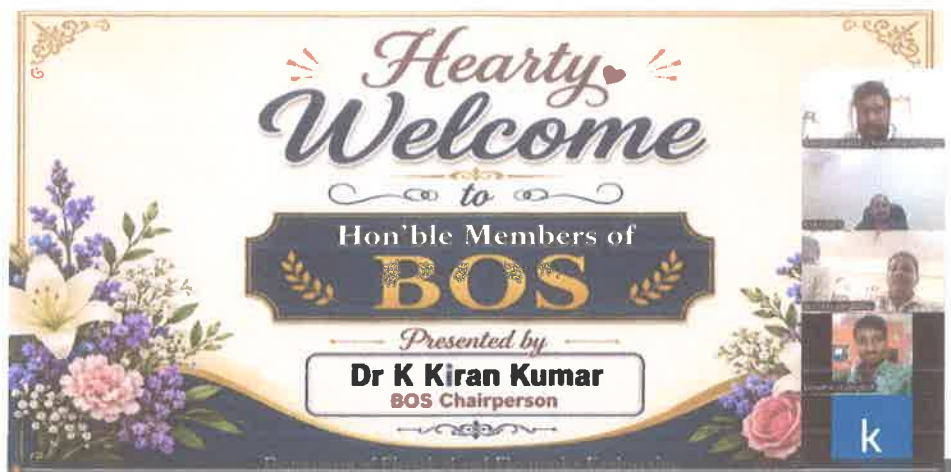
- Update the prescribed textbooks and reference books to include the latest available editions.

6. Course Outcome (CO) Mapping for Laboratory Courses

- Review and update the mapping of Course Outcomes (COs) with **Program Outcomes PO4, PO8, and PO9** for all laboratory courses, wherever applicable.

❖ Mr. Pratap suggested the Following:

- Organizing industry-oriented seminars and workshops to enhance students' practical knowledge and technical skills.
- Providing opportunities for students to gain industry-relevant skills beyond the prescribed syllabus.



Course Structure of I B. TECH V26 Regulations I-SEM

Course Structure

B.Tech. – I Year I Semester (I semester)

S.No.	Course Category	Course Code	Title	L	T	P	C
1	BSC		Linear Algebra and Calculus	3	0	0	
2	BSC		Physics for Electrical Systems	3	0	0	
3	ESC		Computational Thinking and Problem Solving with C	3	0	0	
4	ESC		Basic Electrical Circuits	3	0	0	
5	HSM		English for Technical Communication	3	0	0	
6	BSC		Physics for Electrical Systems Lab	0	0	3	
7	ESC		Computational Thinking and Problem Solving with C Lab	0	0	3	
8	HSM		English for Technical Communications and Moodles Lab	0	0	3	
9	ESC		Basic Electrical Circuits Lab	0	0	3	
10	MNC		NSS / NCC / Community Service	0	0	2	

Online meeting Link:

Sree Vahini Institute of Science and Technology is inviting you to a scheduled Zoom meeting.

Topic: V26- I B. Tech I& II sem CS & Syllabus- Dept of EEE

Time: Oct 1, 2026 03:00 PM India

Join Zoom Meeting

<https://zoom.us/j/97286330203?pwd=aqMdg6YcFiseniWxMpSRaUbP3git6F.1>

Meeting ID: 972 8633 0203

Passcode: 449863

Date: 01-10-2026.


BOS Chairperson
HEAD OF THE DEPARTMENT
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING
VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY
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